



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: TSUTOMU ISHIKAWA, ET AL.)
SERIAL NO.: 09/552,085) Group Art Unit: 2816
FILED: February 24, 2000) Examiner: Nguyen, L.
FOR: SEMICONDUCTOR INTEGRATED)
CIRCUIT HAVING PADS WITH LESS)
INPUT SIGNAL ATTENUATION)

2816
#6/Electus
8/23/01

RESPONSE TO RESTRICTION REQUIREMENT WITH AMENDMENT

The Assistant Commissioner of
Patents and Trademarks
Washington, DC 20231

Dear Sir:

This is a response to an Office Action dated May 18, 2001, wherein the Examiner has subjected claims 1-18 to a Restriction Requirement. Consideration of this Response, entry of the amendment, and prosecution on the merits is requested.

AMENDMENT

Please cancel claims 1-9 without prejudice.

Adjustment date: 03/26/2002 EEKUBAY1
08/24/2001 ASMITH 00000001 061130 09552085
01 FC:117 890.00 CR

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01 FC:117 890.00 CR

03/26/2002 EEKUBAY1 00000007 061130 09552085
01 FC:116 390.00 CR

I HEREBY CERTIFY THAT THIS CORRESPONDENCE
IS BEING DEPOSITED WITH THE UNITED STATES
POSTAL SERVICE AS FIRST CLASS MAIL IN AN
ENVELOPE ADDRESSED TO:
ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D. C. 20231

ON August 15, 2001
Jennifer M. Martin
(TYPED OR PRINTED NAME OF PERSON MAILING PAPER OR FEE)
SIGNATURE Jennifer M. Martin DATE 8/15/01